IN THE CLAIMS

1. (currently amended) A method of generating a project datasheet in an integrated design environment comprising:

accessing project data from an XML database structure, said project data from the integrated design environment and for describing an electronic system design for implementation on a ~~microcontroller programmable system on a chip~~ programmable electronic device;

accessing an XSL stylesheet;

processing said project data according to said XSL stylesheet to automatically produce a project datasheet file.

2. (original) The method of Claim 1, further including formatting said data sheet in HTML.

3. (original) The method of Claim 2, further including rendering said project datasheet as a visual output datasheet using a browser.

4. (original) The method of Claim 1, further including displaying said project datasheet.

5. (original) The method of Claim 4, wherein displaying said project datasheet is done as a single action display.

6. (original) The method of Claim 4, wherein displaying said project datasheet includes printing said project datasheet.

7. (original) The method of Claim 4, wherein said project datasheet includes integrated circuit pinout assignment data.

8. (original) The method of Claim 4, wherein said project datasheet includes a user module schematic.

9. (original) The method of Claim 4, wherein said project datasheet includes global parameters.

10. (original) The method of Claim 4, wherein said project datasheet includes input and output configuration data.

11. (currently amended) A system for automatically generating a project datasheet comprising a computer system, said computer system further comprising:

a database formatted in XML;

an XSL stylesheet directed to a project datasheet; and,

an XSL processor for producing a project datasheet from input from said database and said XSL stylesheet, wherein said datasheet describes an electronic design from an integrated design environment and for implementation on a microcontroller programmable system on a chip programmable integrated circuit device.

12. (original) The system of Claim 11, further including an XSL processor that produces output in HTML format.

13. (original) The system of Claim 11, further including a browser.

14. (original) The system of Claim 11, further including a visual display for displaying said project datasheet.

15. (original) The system of Claim 11, further including a printer for printing said project datasheet.

16. (original) The system of Claim 11, further including an integrated design environment for integrated circuits.

17. (currently amended) A computer readable medium containing executable instructions which, when executed in a processing system, causes the system to perform the steps generating a project datasheet comprising:

accessing project data from an XML database structure, said project data from an integrated design environment and for describing an electronic system design for implementation on a <u>microcontroller programmable system on a chip</u> ~~programmable electronic device~~;

accessing an XSL stylesheet;

processing said project data according to said XSL stylesheet to automatically produce a project datasheet file.

18. (original) The computer readable medium of Claim 17, further including instructions for formatting said project datasheet in HTML.

19. (original) The computer readable medium of Claim 18, further including instructions for rendering said project datasheet using a browser.

20. (original) The computer readable medium of Claim 17, further including instructions for displaying said project datasheet.

21. (original) The computer readable medium of Claim 17, further including instructions for displaying as a single action display.

22. (original) The computer readable medium of Claim 17, further including instructions for printing said project datasheet.

23. (original) The computer readable medium of Claim 17, wherein said project data includes integrated circuit pinout assignment data.

24. (currently amended) A computer controlled method for generating design information comprising:

a) selecting a plurality of global parameters;

b) selecting at least one user module representing a circuit design for a microcontroller programmable system on a chip;

c) placing said user module within a plurality of programmable hardware resources;

d) parameterizing said user module;

e) establishing connections to said user module; and

f) automatically generating a datasheet file describing an electronic design project from an integrated design environment and comprising said user module as parameterized, its connections and said global parameters.

25. (Previously presented) A method as described in Claim 24 further comprising rendering in a visual form said datasheet file.

26. (Previously presented) A method as described in Claim 24 wherein said automatically generating a datasheet file comprises:

f1) accessing project data from an XML database structure;

f2) accessing an XSL stylesheet; and

f3) processing said project data according to said XSL stylesheet to automatically produce said datasheet file.